

FIG. 1

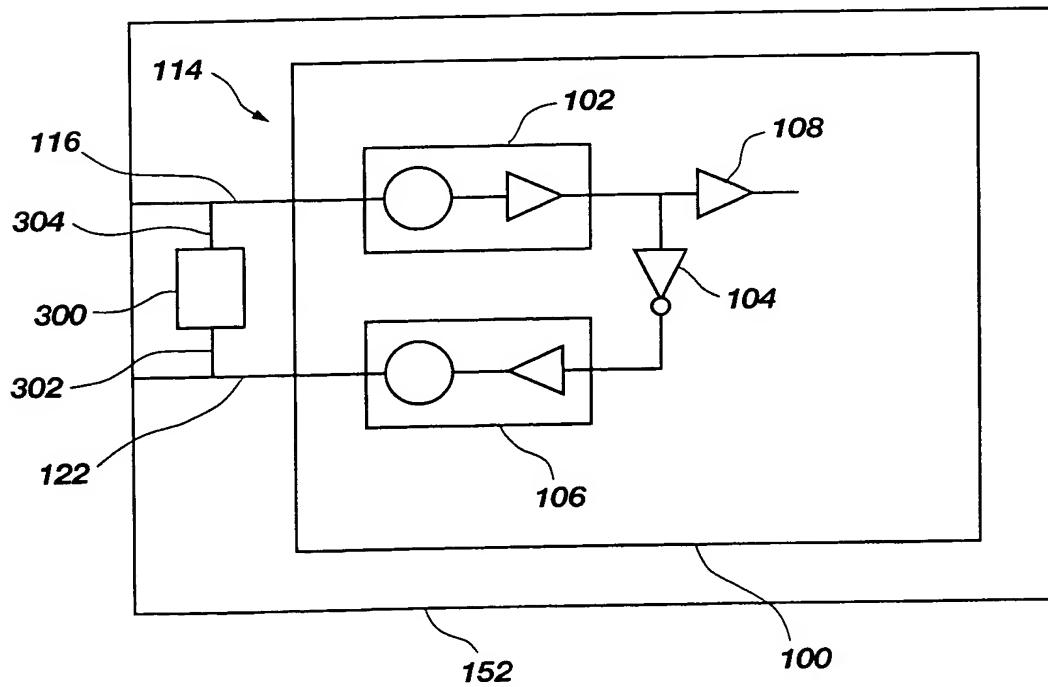


FIG. 2

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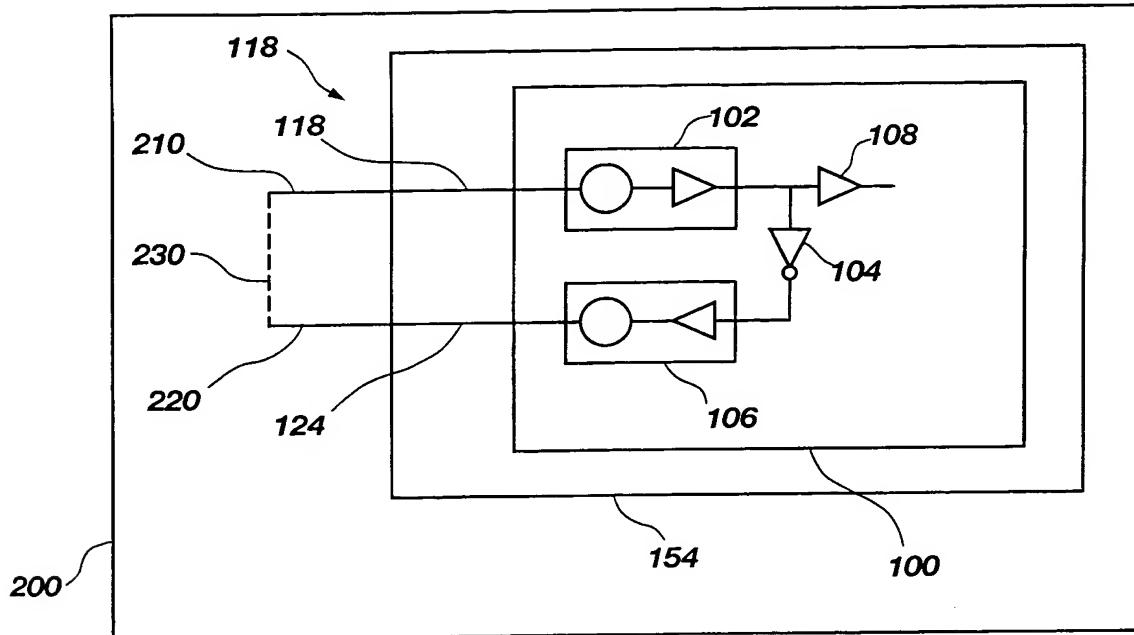


FIG. 3

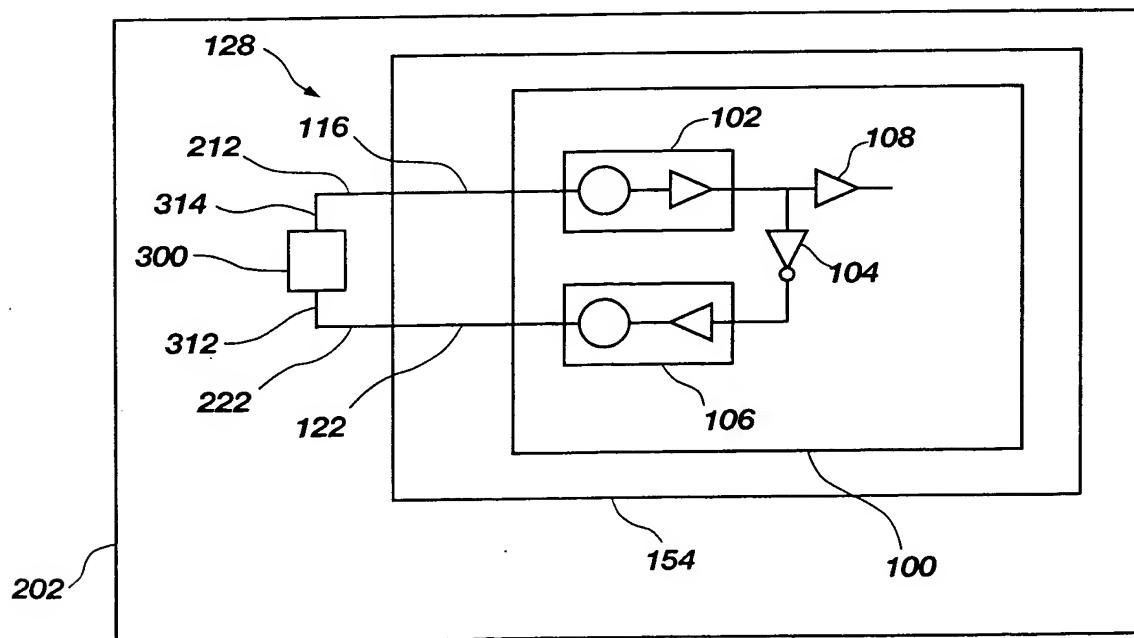


FIG. 4

OSCILLATOR METHOD AND APPARATUS FOR A TEST CHIP

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Attorney/Agent: Morley C. Tobey, Jr.

Telephone: (970) 898-7152

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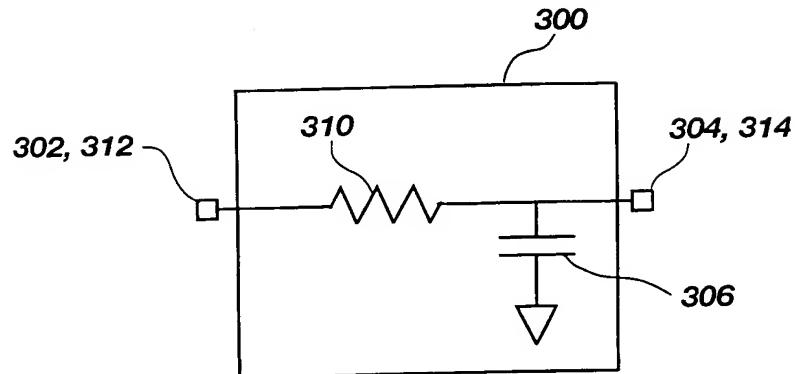


FIG. 5

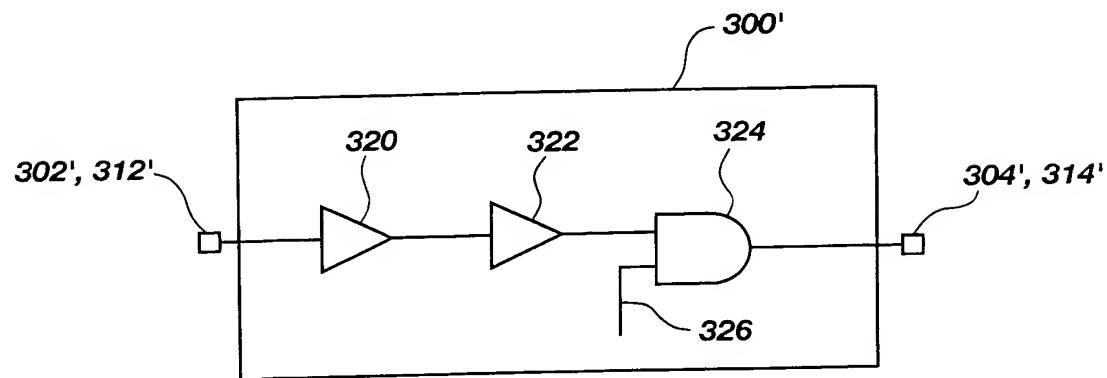


FIG. 6

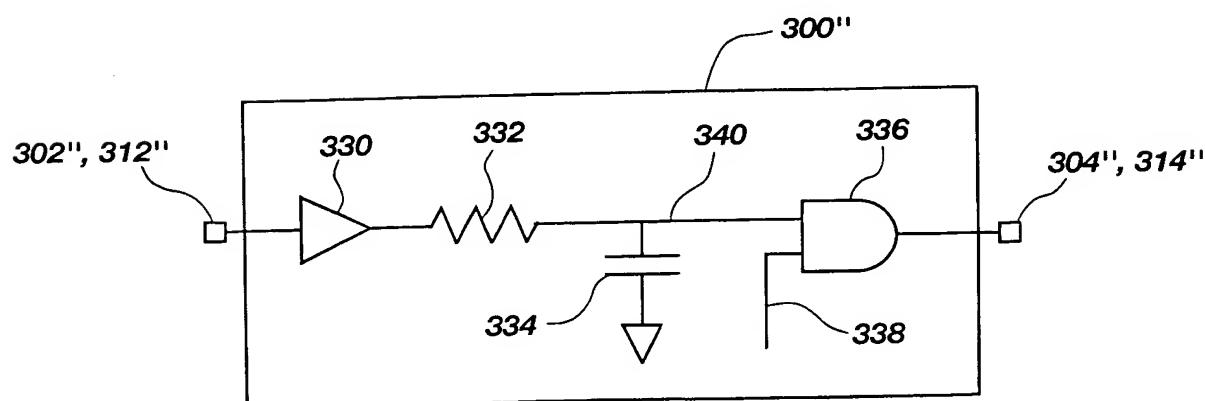


FIG. 7

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